

10072 U.S. PTO

10/051639

01/18/02

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10051639	01/18/2002	257	565	2811 2818	SAG NGUYEN

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2815

Candau

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB DO NOT PUBLISH ☐RESCIND ☐

Foreign priority claimed

☐ yes ☐ no

35 USC 119 conditions met

☐ yes ☐ no

Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

TI-32536

TITLE : ESD improvement by a vertical bipolar transistor with low breakdown voltage and high beta

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	Total Claims <input type="checkbox"/> Print Claim for O.G. <input type="checkbox"/>
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Primary Examiner	Sheets Drawn <input type="checkbox"/> Figs. Drawn <input type="checkbox"/> Print Figs. <input type="checkbox"/>
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner
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